14 Mar 2013 UDC-SA TECHNICAL INFORMATION GUIDE

RS-232 PORT, 7 BIT or 8 BIT PARITY SELECTION, DIP SWITCH SW2, PIN 8

SW2, Pin 8 ON = 8, N, 1 SW2, Pin 8 OFF = 7, 0, 1

DIP SWITCH SW2, PINS 1 (SYNC ENABLE)

Enable SYN character (16h) recognition SW2 Pin 1 (Set to ON)

SYNC Control Charactor SW2, PINS 2-4

Enable U character (55h) recognition SW2 Pin 2 Enable T character (54h) recognition SW2 Pin 3 Enable S character (53h) recognition SW2 Pin 4

Input side

RS530 DTE

1200bps Synchronous 8bit character

Receive data input (pins 3/16)

DCD control signals (pins 8/10) are high when data is being received

CTS control signal (pins 5/13) pass through to output side

DSR control signal (pins 6/22) pass through to output side

Signal ground (pin 7)

Receive clock (pins 17/9) can either be gated or continuous

Transmit clock (pins 15/12) can either be gated or continuous

DTR control signal (pins 20/23) controlled by input side DTR (pin 20)

RTS control signal (pins 4/19) controlled by input side RTS (pin 4)

Max message size is 26000 characters

Start of data processing indication DCD control signal transitions from low to high

Start of message sync string to look for in receive data is 3 ASCII characters (used to identify first and last bit of the synchronous characters for adding async start and stop bits to output data)

Sync Strings

SYNC control character (SYNSYNSYN) Hex 016 x3

Capital S's (SSS) Hex 053 x3

Capital T's (TTT) Hex 054 x3

Capital U's (UUU) Hex 055 x3

sync strings DIP switch selectable

Output side

RS232 DCE

2400 Asynchronous Start bit 8 bit character no parity 1 stop bit(switch selectable)

2400 Asynchronous Start bit 7 bit character odd parity 1 stop bit(switch selectable)

Receive data output (pin 3)

DCD control signal (pins 8) controlled by input side DCD (output needs to be in sync with receive data)

CTS control signal (pins 5) controlled by input side CTS

DSR control signal (pins 6) controlled by input side DSR

Signal ground (pin 7)

Receive clock N/A

Transmit clock N/A

DTR control signal (pin 20) pass through to output side DTR (pins 20/23)

RTS control signal (pin 4) pass through to output side RTS (pins 4/19)

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